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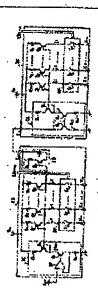
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(54) 【発明の名称】 ディスプレイ執道及びディスプレイパネルの戦闘周路 (57) 【契约】

「課題」 時候終ドライブ関係を報数の1 Cチップで構築した際にもディスプレイパネル上での発光探索を均一にすることが出来るディスプレイパネル上での発光探索を均一にすることが出来るディスプレイパネルの観光音を発光を対して、このディスプレイパネルの第1番を検生では8ずる複数の発光を対して、このディスプレイパネルの第1番接換に登8ずる複数の発光を対して、このディスプレイパネルの第1番接換に登8ずる複数の発光を対して、このディスプレイパネルの第1番接換に登8ずる複数の疑動医路からなり、これら報動的の内の少なくとも1には、他の経動器時が発生した発光報動電流に基づいて、この経動四段が完全すべき発光を動電流に基づいて、この経動四段が完全すべき発光を動電流の電流量の放発を行う限動電流制の回路が設けられている。



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(71)Applicant : PIONEER ELECTRONIC CORP

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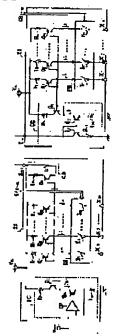
OCHI HIDEO "

(54) DISPLAY DEVICE AND DRIVING CIRCUIT OF DISPLAY PANEL

(57)Abstract:

PROBLEM TO BE SOLVED: To make light emitting luminance on a display possible to be uniform at the time of constituting an anode line drive circuit of plural IC chips by controlling the amount of light emitting driving current to be outputted by a first anode line driven circuit based on the light emitting current outputted by a second anode line drive circuit.

SOLUTION: A current source (transistors Q1 to Qm) is provided in anode line drive circuits 21 and 22 to generate a light emitting driving current. Moreover, a driving current control circuit CC, which maintains the light emitting driving current at an amount of current corresponding to an inputted control current, is provided. Furthermore, a control current output circuit CO is provided to output a light emitting driving current itself as a control current. At the time of driving anode lines of a display panel by plural anode line drive circuits 21 and 22 constructed in individual IC chips, the first and anode line driving circuit 21 controls the amount of light



emitting driving current to be outputted based on the actually outputted light emitting drive current by a second anode line drive circuit 22.

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